



Concurrent Models of Computation for Embedded Software

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Lecture 0: Introduction

Logistics

Class web page:

<http://embedded.eecs.berkeley.edu/concurrency>

Project

Paper (and paper review)

Homework

Study group

Technology:

- Ptolemy II
- Java
- Eclipse
- LaTeX

Homework

- Issued roughly every two weeks
- Will leverage a common technology base:
 - Java
 - Eclipse
 - Ptolemy II
- First assignment is on the web

Lee 01: 3

Project

- Conference (workshop) paper quality expected
- Papers will be “submitted” and “reviewed” by you
- Presentations will be workshop like
- Teams up to two are encouraged
- Leveraging the technology base is encouraged
- Many project suggestions are on the web
 - In almost all cases, I have a fairly clear idea of how to start. Come talk to me if one of these looks interesting

Lee 01: 4

Study Group

- A mechanism for reading and discussing papers.
- Hopefully will meet Fridays, 4-5PM, DOP center classroom.
- Each week, four students are assigned to lead the discussion. One of those will be selected as the overall coordinator.
- All are expected to have read the paper before the study group meets.
- All are encouraged to comment, as questions, and participate in discussion.
- Come prepared with a hard or soft copy of the paper.

Lee 01: 5

Introduction to Edward A. Lee

- Working in embedded software since 1980, when I was writing assembly code for the AT&T DSP-1 to implement modems at Bell Labs.
- BS '79 (Yale)
SM '81 (MIT)
PhD '86 (Berkeley)
- Berkeley EECS faculty since 1986
- One of four directors of Chess, the Berkeley *Center for Hybrid and Embedded Software Systems*
- Director of the Berkeley Ptolemy project
- Co-author of five books (on digital communications, signals and systems, and dataflow)
- Co-founder of BDTI, Inc., a 12 year old technology company
- Key awards:
 - NSF Presidential Young Investigator
 - Terman Award for Engineering Education.
 - Fellow of the IEEE



Lee 01: 6

Who are you?